

## INFORMATION DISCLOSURE STATEMENT

APPLICANT:

Stephen J. Whitney

DOCKET NO.:

112690-004

SERIAL NO:

09/649,195

ART UNIT:

2836

FILED:

August 28, 2000

**EXAMINER:** 

Unknown

INVENTION:

"INTEGRATED APPARATUS 1

OVERCURRENT

AND

THE

OVERVOLTAGE

PROTECTION OF

TELECOMMUNICATION CIRCUITS"

**Assistant Commissioner for Patents** 

Washington, D.C. 20231

SIR:

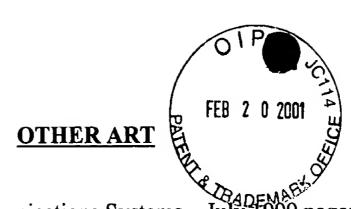
In accordance with the provisions of 37 C.F.R. 1.56, Applicants request that a citation and examination of the references identified below and on the attached PTO-1449 form, copies of which can be found in the parent application Serial No. 09/534,277, filed March 24, 2000, in accordance with 37 C.F.R. 1.98, be made during the course of examination of the above-mentioned application for United States Patent.

## **U.S. PATENTS**

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Harris Surgectors for Telecommunications Systems – July 1999 pages 10- 149/10-156

Applicants look forward to early and favorable consideration of this matter.

Respectfully submitted,

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. 112690-004
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In Re Application Of: St	ephen J. Whitney	(C)	
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Serial No.	Filing Date	Examiner	Group Art Unit
09/649,195	Filing Date August 28, 2000	PADEMARY Unknown	2836
Title:			
INTEGRATED OVERCU	RRENT AND OVERVOLTAGE	APPARATUS FOR USE IN TH	IE PROTECTION OF
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